

35174 U.S. PTO
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35174

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10064561	FILING DATE 07/26/2002	CLASS 438	SUBCLASS 300	GAU 2812	EXAMINER Lindsay
**APPLICANTS: Chang Kent;					
**CONTINUING DATA VERIFIED: None will					
** FOREIGN APPLICATIONS VERIFIED: TAIWAN 91114489 07/01/2002 will					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's Initials will				ATTORNEY DOCKET NO 9167-US-PA	
TITLE : Method for fabricating raised source/drain of semiconductor device <small>U.S. DEPT. OF COMM. / PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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